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| --- | --- |
| **Module Name** | **Link Description** |
| TLB (Target Look aside Buffer) | <https://www.geeksforgeeks.org/translation-lookaside-buffer-tlb-in-paging/>  <https://techpubs.jurassic.nl/manuals/hdwr/developer/R10K_UM/sgi_html/t5.Ver.2.0.book_353.html#:~:text=Three%20types%20of%20TLB%20exceptions%20can%20occur%3A&text=TLB%20Invalid%20occurs%20when%20a,the%20entry%20is%20not%20writable>  <https://www.youtube.com/watch?v=Z2T2vnyZl0o> |
| Priority Encoder | <https://www.youtube.com/watch?v=kEj-m3YuGa4&feature=youtu.be> |
| BTB(Branch Target Buffer) | <https://www.sciencedirect.com/topics/computer-science/branch-target-buffer> |
| BHT (Branch History Table) | <https://www.youtube.com/watch?v=yk-U6qqeGE0> |
| NPC | <https://www.embecosm.com/appnotes/ean7/html/ch04s06s01.html> |
| CSR (Control and State Register) | <https://people.cs.pitt.edu/~don/coe1502/current/Unit4a/Unit4a.html> |
| Cache | <https://www.student-circuit.com/learning/year3/embedded-systems/what-are-three-types-of-cache-memory/> |
| Scratchpad memory (SPRAM) | <https://www.sciencedirect.com/topics/computer-science/scratchpad-memory> |
| Control Flow Integrity (CFI) | <https://www.youtube.com/watch?v=LKK7U_gF744> |
| AMBA | <https://www.allaboutcircuits.com/technical-articles/introduction-to-the-advanced-microcontroller-bus-architecture/> |
| AXI | <https://www.allaboutcircuits.com/technical-articles/introduction-to-the-advanced-extensible-interface-axi/> |
| Aborts and Pre-fetch Aborts | <https://developer.arm.com/documentation/ddi0344/c/programmer-s-model/exceptions/aborts> |
| CLINT | <https://chromitem-soc.readthedocs.io/en/latest/clint.html> |
| Coherent Memory | <https://whatis.techtarget.com/definition/memory-coherence> |
| SISD and SIMD | <https://www.quora.com/What-is-the-difference-between-SISD-and-SIMD> |
| Superscalar processor | <https://www.slideshare.net/ManashKumarMondal/superscalar-processor> |
| Position Independent Code (PIC) | <https://docs.oracle.com/cd/E26505_01/html/E26506/glmqp.html> |
| Adding GPIOS with rocket chip | <https://github.com/sifive/sifive-blocks/tree/master/src/main/scala/devices> |